

Docket No.: 67161-057

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of :  
:   
Hideto HIDAKA :  
:   
Serial No.: : Group Art Unit:  
:   
Filed: July 22, 2003 : Examiner:  
:   
For: THIN FILM MAGNETIC MEMORY DEVICE PROVIDED WITH PROGRAM  
ELEMENT

**INFORMATION DISCLOSURE STATEMENT**

Mail Stop Patent Application  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

The relevance of JP 2002-117684 and SCHEUERLEIN, et al. is discussed in the present specification.

INFORMATION DISCLOSURE CITATION IN AN APPLICATION						ATTY. DOCKET NO. <b>67161-057</b>		SERIAL NO.			
(PTO-1449)						APPLICANT <b>Hideto HIDAKA</b>					
						FILING DATE <b>July 22, 2003</b>		GROUP			
U.S. PATENT DOCUMENTS											
EXAMINER'S INITIALS		CITE NO.	Document Number Number-Kind Code <sub>2</sub> ( <i>if known</i> )		Publication Date MM-DD-YYYY		Name of Patentee or Applicant of Cited Document		Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		
	A1	US	2003/0090935		05/15/2003		HIDAKA				
		US	10/396,481		03/26/2003		OOISHI				
		US	10/387,536		03/14/2003		OHTANI				
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FOREIGN PATENT DOCUMENTS											
EXAMINER'S INITIALS		CITE NO.	Foreign Patent Document Country Codes - Number + Kind Codes ( <i>if known</i> )		Publication Date MM-DD-YYYY		Name of Patentee or Applicant of Cited Document		Pages, Columns, Lines Where Relevant Figures Appear	Translation	
			JP 2002-117684		04/19/2002		PERNER, et al.			Yes	No
										abstract	
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)											
EXAMINER'S INITIALS		CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.								
			SCHEUERLEIN, et al. "A 10ns Read and Write Non-Volatile Memory Array Using a Magnetic Tunnel Junction and FET Switch in Each Cell" IEEE International Solid-State Circuits Conference (2000) TA 7.2.								
			DURLAM, et al. "Nonvolatile RAM Based on Magnetic Tunnel Junction Elements" IEEE International Solid-State Circuits Conference (2000) TA 7.3.								
			NAJI, et al. "A 256kb 3.0V 1T1MJT Nonvolatile Magnetoresistive RAM" IEEE International Solid-State Circuits Conference (2001) 7.6.								
EXAMINER						DATE CONSIDERED					

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.

**Serial No.:**

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

MCDERMOTT, WILL & EMERY

A handwritten signature in black ink, appearing to read "Becker", with a long horizontal stroke extending to the right.

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